



# SERGE SEQUENTIAL PULSE SOURCE

1999. Drawn by Marjan Urekar

THIS IS BASIC 10-STEP SEQUENCER WITH NEED OF EXTERNAL CLOCK. SEQUENCE CAN BE 1-10 STEPS LONG, PATCH CONNECTION FROM OUTPUT OF LAST DESIRED STEP TO RESET INPUT. OUTPUTS ARE 5V PULSES. VOLTAGE DIVIDER POT COULD FOLLOW AFTER EACH TRANSISTOR OUTPUT SECTION TO OBTAIN CVS. GLOBAL GATE SIGNAL COULD BE TAKEN FROM PIN 15 OF U1.

+V = +6V

IN ORIGINAL:  
 U1 = 34017  
 NPNs = 2N3565  
 PNP = 2N4253  
 DIODES = 1N914